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Shams, M.; Ebergen, J.C.; Elmasry, M.I.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume: 6 , Issue: 4 , Dec. 1998
Pages:563 - 567

[Abstract] [PDF Full-Text (84KB)] IEEE JNL

2 On the delay-sensitivity of gate networks

Brzozowski, J.A.; Ebergen, J.C.;
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3 DUES: a fault abstraction and collapsing framework for asynchronous circuits

Shirvani, P.P.; Mitra, S.; Ebergen, J.C.; Roncken, M.;
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4 Optimizing CMOS implementations of the C-element

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5 A comparison of CMOS implementations of an asynchronous circuits primitive: the C-element

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6 Predicting performance of micropipelines using Charlie diagrams

Ebergen, J.C.; Fairbanks, S.; Sutherland, I.E.;
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Pages:238 - 246

[\[Abstract\]](#) [\[PDF Full-Text \(116KB\)\]](#) IEEE CNF

7 Delay-insensitive solutions to the committee problem

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8 A verifier for network decompositions of command-based specifications

Ebergen, J.C.; Gingras, S.;

System Sciences, 1993, Proceeding of the Twenty-Sixth Hawaii International
Conference on , Volume: i , 5-8 Jan 1993

Pages:310 - 318 vol.1

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Electronics Letters, Volume: 40, Issue: 5, 4 March 2004

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2 A VLSI algorithm for modular multiplication/division

Kaihara, M.E.; Takagi, N.;

Computer Arithmetic, 2003. Proceedings. 16th IEEE Symposium on, 15-18

June 2003

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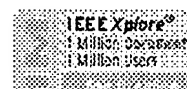
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Yan, Z.; Sarwate, D.V.;

Computers, IEEE Transactions on , Volume: 52 , Issue: 11 , Nov. 2003

Pages:1514 - 1519

[\[Abstract\]](#) [\[PDF Full-Text \(585 KB\)\]](#) **IEEE JNL****2 High-speed easily testable Galois-field Inverter**

Chih-Tsun Huang; Cheng-Wen Wu;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE

Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Volume: 47 , Issue: 9 , Sept. 2000

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